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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

JC903 U.S. PTO
10/073999

11/14/02

APPL. NO.	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10073999	02/14/2002	365	165.1	2818	H0, H0A1

** APPLICANTS: Takeuchi Ken; Tanaka Tomoharu; Shibata Noboru;

** CONTINUING DATA VERIFIED:

THIS APPLICATION IS A DIV OF 09/667,610 09/22/2000 U.S. PAT. NO. 6,373,746

** FOREIGN APPLICATIONS VERIFIED:

JAPAN 11-275327 09/28/1999

JAPAN 11-345299 12/03/1999

PG-PUB DC NOT PUBLISH ☐RESCIND ☐

Foreign priority claimed

☐ yes ☒ no

35 USC 119 conditions met

☐ yes ☒ no

Verified and Acknowledged Examiners's Initials

ATTORNEY DOCKET NO

001701.00140

TITLE : Nonvolatile semiconductor memory having plural data storage portions for a bit line connected to memory cells

U.S. DEPT. OF COMM. / PAT. & TM-PTO-436L (Rev. 12-91)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
Assistant Examiner		DRAWING	
		Sheets Drwg.	Figs. Drwg.
Primary Examiner		Print Fig.	
PREPARED FOR ISSUE		Application Examiner	
ISSUE FEE		WARNING: The information disclosed herein may be restricted.	
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